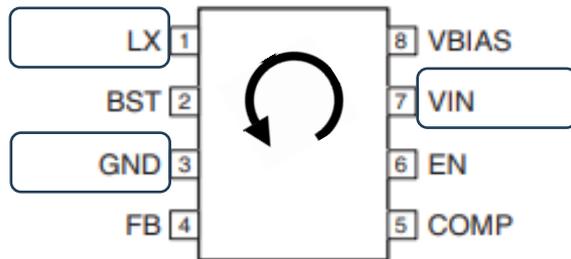


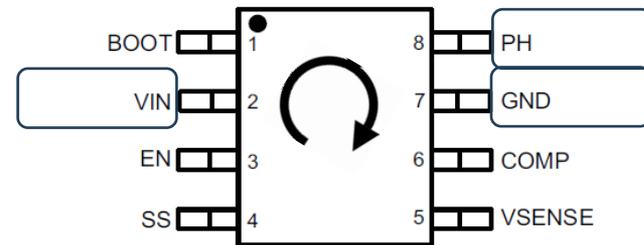
Beispiel DC/DC-Wandler

AOZ1210



4.5 ... 27 V, 3 A
70 mΩ, 370 kHz

TPS54331



3.5 ... 28 V, 3 A
80 mΩ, 570 kHz

beide im SO-8 Gehäuse, ohne Powerpads

Layout-Vorschlag im AOZ1212 Datenblatt

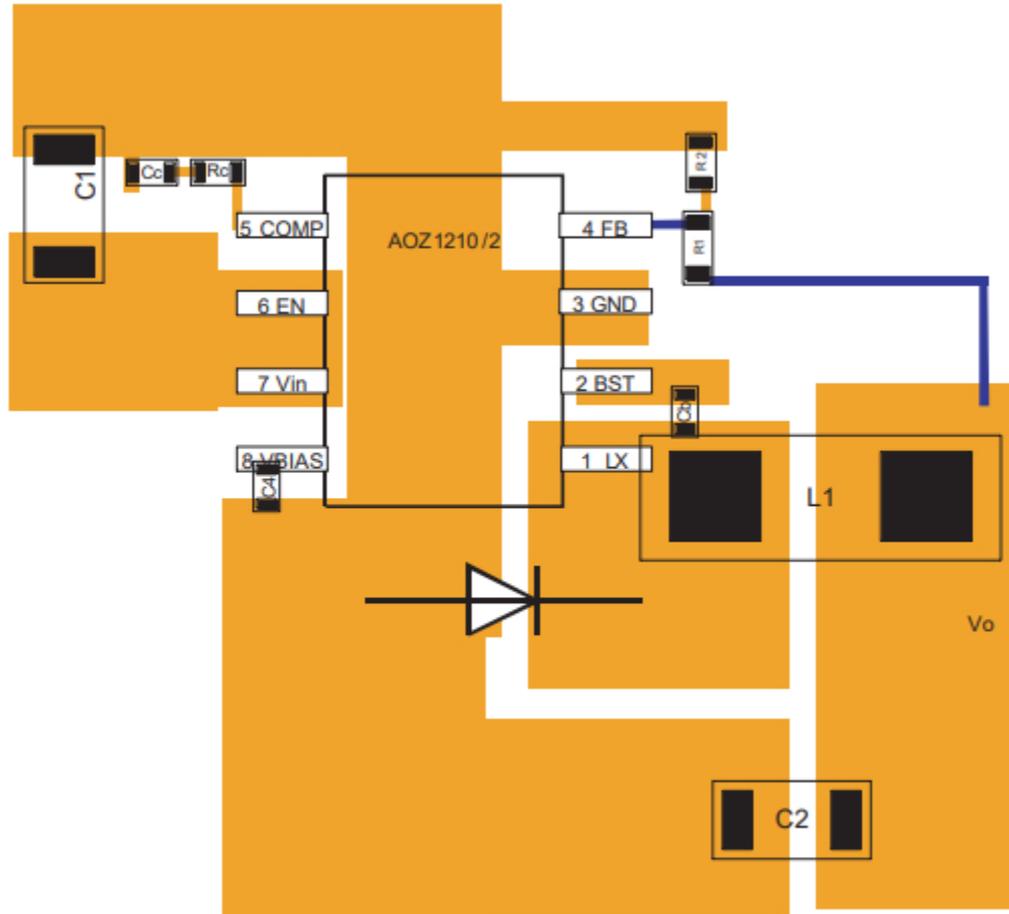


Figure 3. Layout Example of the AOZ1210

Layout-Vorschlag im TPS54331 Datenblatt

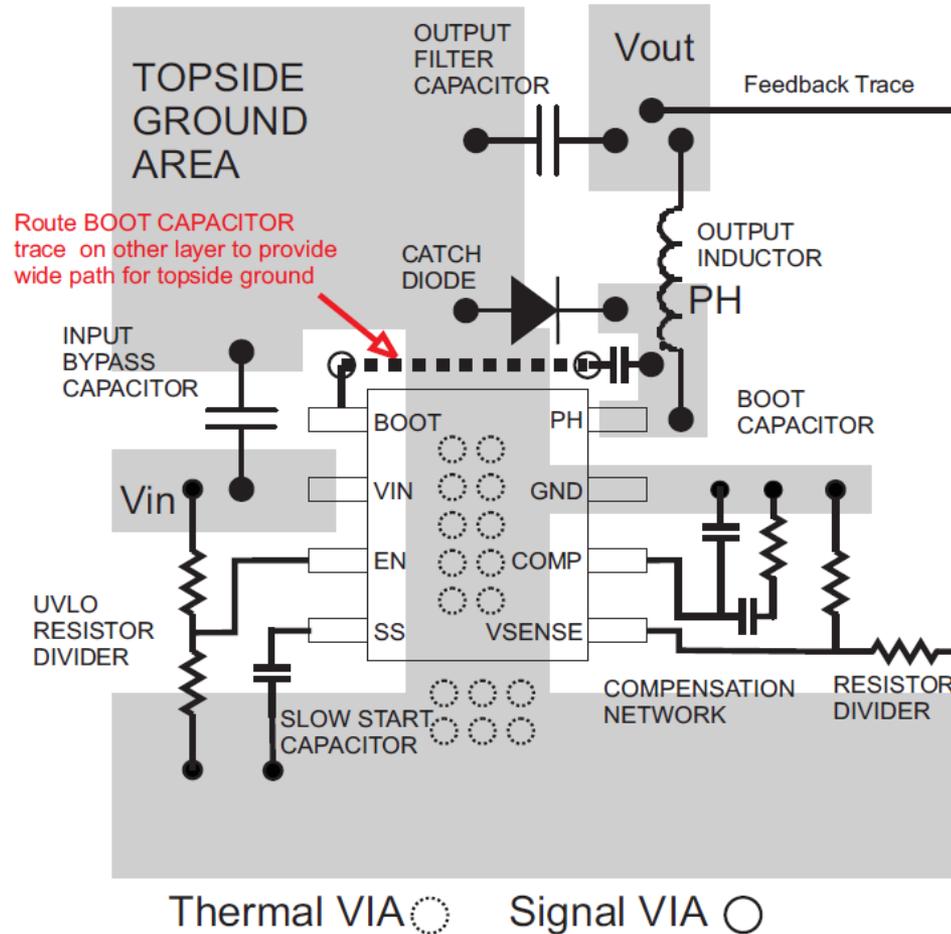
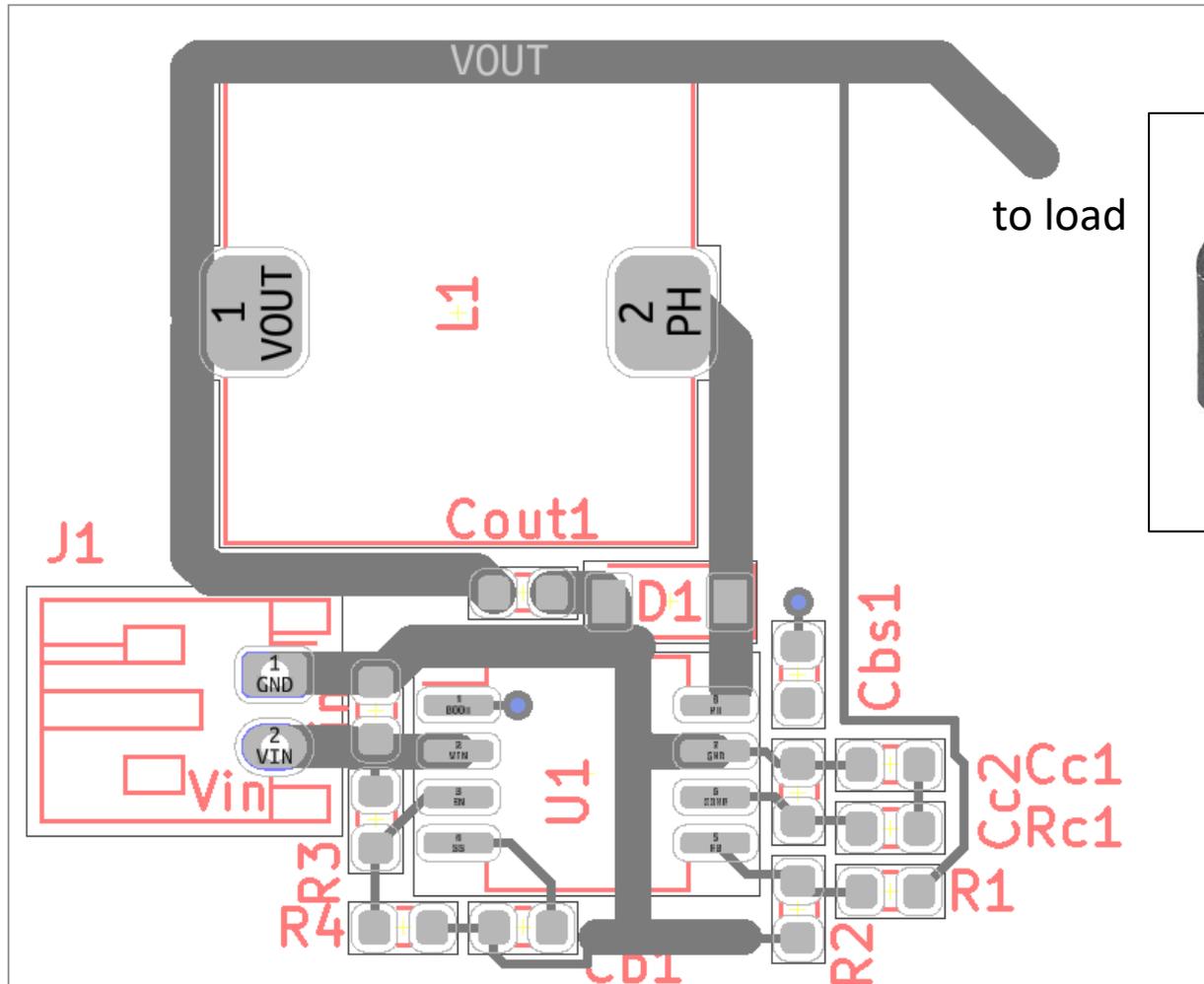
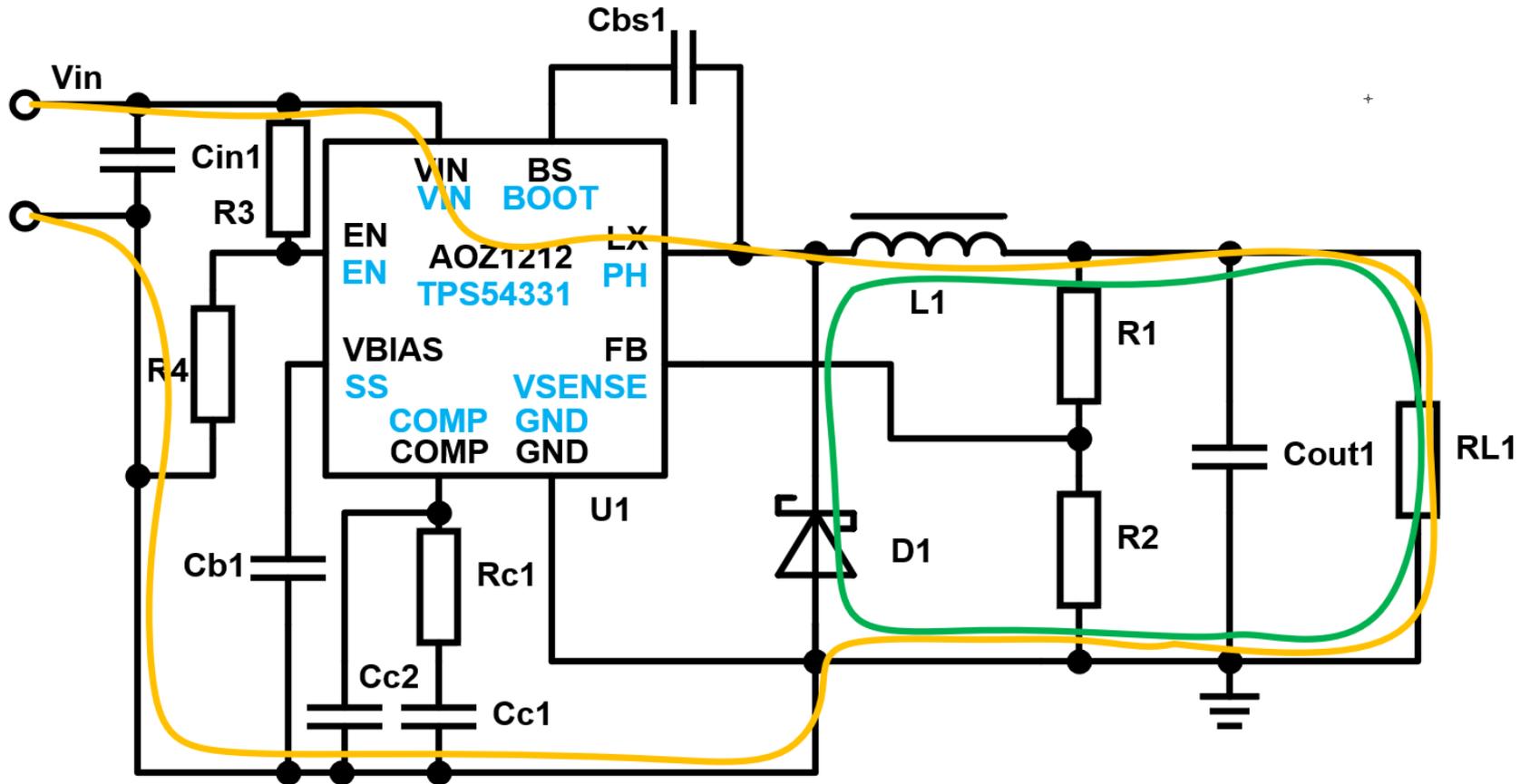


Figure 10-1. TPS54331 Device D Board Layout

TPS54331 Layout Beispiel - Top



»Hot Loops«



»Hot Traces« mit Beispielwerten

